

IN THE SPECIFICATION:

Please substitute the following paragraph for the paragraph starting at page 10, line 7 and ending at line 17.

Figure 3 is a block diagram of a system for implementing a fractional sample delay according to the present invention. A correlator 2 correlates the signal samples received by a receiver with a reference training signal to produce correlation values. A verification device 4, such as a processor, selects only those correlation values that are above the threshold value. A determination device 6, such as a processor, determines if a fractional sample delay is needed and calculates the required fractional sample delay using the correlation values that exceed the threshold value. An implementation device 8 then implements the fractional sample delay, when necessary, and the data is set to a demodulating device (~~not shown~~) 10, such as processor executing software or a digital or analog circuit, for demodulating.